

UNITED STATES PATENT AND TRADEMARK OFFICE



UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER OF PATENTS AND TRADEMARKI;
Washington, D.C. 20231
www.uspto.gov

DATE MAILED: 01/23/2003

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/851,387	05/09/2001	Mitsuhiro Nada	205002US2	3902
22850	7590 01/23/2003			
OBLON, SPIVAK, MCCLELLAND, MAIER & NEUSTADT, P.C. 1940 DUKE STREET ALEXANDRIA, VA 22314			EXAMINER	
			VERBITSKY, GAIL KAPLAN	
			ART UNIT	PAPER NUMBER
			2859	

Please find below and/or attached an Office communication concerning this application or proceeding.

M;	/
----	---

Office Action Summary

Application No. 09/851,387

Applicant(s)

Nada

Examiner

Gail Verbitsky

Art Unit 2859



	, ,	on the cover sheet with the correspondence address			
	for Reply				
	IORTENED STATUTORY PERIOD FOR REPLY IS SET MAILING DATE OF THIS COMMUNICATION.	TO EXPIRE MONTH(S) FROM			
	sions of time may be available under the provisions of 37 CFR 1.136 (a).	In no event, however, may a reply be timely filed after SIX (6) MONTHS from the			
- If the - If NO - Failure - Any re	period for reply specified above is less than thirty (30) days, a reply within	bly and will expire SIX (6) MONTHS from the mailing date of this communication. se the application to become ABANDONED (35 U.S.C. § 133).			
Status					
1)💢	Responsive to communication(s) filed on Dec 19, 2	2002			
2a) 🗌	This action is FINAL . 2b) ☑ This act	tion is non-final.			
3)□	Since this application is in condition for allowance e closed in accordance with the practice under Ex par	except for formal matters, prosecution as to the merits is rte Quayle, 1935 C.D. 11; 453 O.G. 213.			
Disposi	ition of Claims				
4) 💢	Claim(s) <u>1-3, 7-9, and 14</u>	is/are pending in the application.			
4	4a) Of the above, claim(s) 4-6, 1/-13	is/are withdrawn from consideratio			
5) 🗆	Claim(s)	is/are allowed.			
6) 💢	Claim(s) <u>1-3, 7-9, and 14</u>	is/are rejected.			
7) 🗆		is/are objected to.			
8) 🗆		are subject to restriction and/or election requirement			
Applica	ation Papers				
9) 🗆	The specification is objected to by the Examiner.				
10)	The drawing(s) filed on is/ar	re a \square accepted or b \square objected to by the Examiner.			
	Applicant may not request that any objection to the di	rawing(s) be held in abeyance. See 37 CFR 1.85(a).			
11)	The proposed drawing correction filed on	is: aD approved bD disapproved by the Examine			
	If approved, corrected drawings are required in reply t	to this Office action.			
12)	The oath or declaration is objected to by the Exami	iner.			
	under 35 U.S.C. §§ 119 and 120				
13) Acknowledgement is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).					
	☑ All b)☐ Some* c)☐ None of:				
	1. X Certified copies of the priority documents have been received.				
	2. Certified copies of the priority documents have				
	3. Copies of the certified copies of the priority do application from the International Burea				
* S	ee the attached detailed Office action for a list of the				
14)	Acknowledgement is made of a claim for domestic	priority under 35 U.S.C. § 119(e).			
a) [
15)	Acknowledgement is made of a claim for domestic	priority under 35 U.S.C. §§ 120 and/or 121.			
Attachm					
	otice of References Cited (PTO-892)	4) Interview Summary (PTO-413) Paper No(s).			
	otice of Draftsperson's Patent Drawing Review (PTO-948)	5) Notice of Informal Patent Application (PTO-152)			
_3) [X] Inf	formation Disclosure Statement(s) (PTO-1449) Paper No(s). 35	6) Other:			

Art Unit: 2859

DETAILED ACTION

1. Applicant's election of Species A (Claims 1-3, 7-10, 14) with traverse is hereby acknowledged. Applicant states that a search for elected species will also necessarily require a search for other species. Examiner respectfully disagrees with this statement, because a search required for the elected species (power semiconductor/ coolant) does not require a search in the area of electrical motors, as required by non-elected claims. Therefore, restriction/ election requirement is proper and thereby, made FINAL.

Priority

Acknowledgment is made of applicant's claim for foreign priority under 35 U.S.C. 119 (a)-(d).

Claim Rejections - 35 USC § 102

3 The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 4. Claims 1-3 are rejected under 35 U.S.C. 102(b) as being anticipated by Mori et al. (U.S. 5778662) [hereinafter Mori].

Art Unit: 2859

Mori discloses a device and a method of determining (estimating) temperature, the device comprises of a first object/ component (coolant/ heat sink), a second object/ component (power semiconductor/ transistor), wherein, before energization, both objects temperatures are equalized by the coolant. The method comprises determining of the temperature of the coolant Ts (T_2) . The computer adds the Ts (T_2) and Tds $(\triangle T)$ which is the difference in temperatures between the coolant and the transistor. Inherently, that the both objects are positioned relatively close to each other and that in absence of a heat generation (energization) their temperatures equalize. Inherently, the temperature difference is related (characterizes) the amount of heat (energization) needed to energize (turn in) the transistor.

With respect to claims 1-3: the method steps will be met during the normal operation of the device stated above.

5. Claims 1-3 are rejected under 35 U.S.C. 102(b) as being anticipated by Takeda (U.S. 5923135).

Takeda discloses a device and a method of determining (estimating) temperature, the device comprises a first object/ component (coolant), a second object/ component (power semiconductor), wherein, before energization, both objects temperatures are equalized by the coolant. The coolant temperature $Tc(T_2)$ is being detected by a temperature sensor 14. A temperature of the semiconductor (junction) is being determined by a formula $Tj = P \cdot 01 + Tc$, wherein, $P \cdot 01$ (ΔT or increment) indicates the amount of energization needed for junction

Art Unit: 2859

(specific value). It is inherent that ΔT should be determined prior to incorporating it into the formula shown above. There is an estimator to estimate the junction (T_1) temperature.

With respect to claims 1-3: the method steps will be met during the normal operation of the device stated above.

6. Claims 1-3, 8 are rejected under 35 U.S.C. 102(b) as being anticipated by DE 19852080 C1 {hereinafter DE}.

DE discloses a device and method comprising a first object (coolant) a second object (power semiconductor) wherein, when a temperature of equilibrium between the coolant and the semiconductor is reached, a temperature of the coolant T m (T_1) is measured. A temperature of the semiconductor T_2 is determined by adding a temperature difference value $(\triangle T)$ to the detected temperature T_1 of the coolant. The temperature difference (specific value) is computed using pre-existing relationship between a power loss and difference after a change in a power loss (energization). DE determines the temperature of the coolant at a detection point that reaches equilibrium temperature after a change in power loss, thus, in a state when the power semiconductor is not energized. Furthermore, it is inherent that power semiconductors response more rapid (junction) to a heat change than a coolant.

With respect to claims 1-3: the method steps will be met during the normal operation of the device stated above.

Art Unit: 2859

Claim Rejections - 35 USC § 103

- 7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 8. Claims 7-9 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over DE in view of Eisenhardt et al. (U.S. 6348672B2) [hereinafter Eisenhardt].

DE discloses a device and method comprising a first object (coolant) a second object (power semiconductor) wherein, when a temperature of equilibrium between the coolant and the semiconductor is reached, a temperature of the coolant T m (T_1) is measured. A temperature of the semiconductor T_2 is determined by adding a temperature difference value (ΔT) to the detected temperature T_1 of the coolant. The temperature difference (specific value) is computed using pre-existing relationship between a power loss and difference after a change in a power loss (energization). DE determines the temperature of the coolant at a detection point that reaches equilibrium temperature after a change in power loss, thus, in a state when the power semiconductor is not energized. Inherently, a determination (estimation portions) to determine the temperature of the coolant T_2 should be present in the device. It is also inherent, that both objects are positioned relatively close to each other, so as the coolant could cool the power

Art Unit: 2859

semiconductor. Furthermore, it is inherent that power semiconductors response more rapid

(junction) to a heat change than a coolant.

DE does not explicitly teach measuring the temperature of the power semiconductor, as

Page 6

stated in claim 7, and a temperature determination portion and an estimation portion, as stated in

claim 14.

Eisenhardt discloses a device comprising a first object (coolant) to cool a second object

(power semiconductor) wherein the power semiconductor temperature is being measured by a

temperature sensor (temperature determination portion) integrated (installed) in the power

semiconductor.

Therefore, it would have been obvious to one of ordinary skill in the art at the time the

invention was made to integrate a temperature sensor in a power semiconductor of DE, as taught

by Eisenhardt, so as to obtain instantaneous temperature measurement of the power

semiconductor, in order to allow the operator to take immediate necessary actions if the power

semiconductor fails.

Conclusion

9. The prior art made of record and not relied upon is considered pertinent to applicant's

disclosure. The prior art cited in the PTO-892 and not mentioned above disclose related devices

and methods.

Art Unit: 2859

10. Any inquiry concerning this communication should be directed to Examiner Verbitsky who can be reached at (703) 306-5473 Monday through Friday 7:30 to 4:00 ET.

Any inquiry of general nature should be directed to the Group Receptionist whose telephone number is (703) 308-0956.

GKV

January 06, 2002

Gail Verbitsky, Patent Examiner, TC 2800

6. Ovelester